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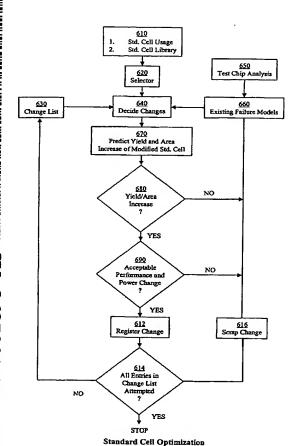
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[Continued on next page]

(54) Title: YIELD IMPROVEMENT



(57) Abstract: An integrated circuit is designed to improve yield when manufacturing the integrated circuit, by obtaining a design element from a set of design elements used in designing integrated circuits (610). A variant design element is created based on the obtained design element, where a feature of the obtained design element is modified to create the variant design element (670). A yield to area ratio for the variant design element is determined (680). If the yield to area ratio of the obtained design element, the variant design element is retained to be used in designing the integrated circuit (612).



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	column 3 line 5 to column 8, line 38; Figs. 1-6.		37-47, 49-53, 57, 65- 67					
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